
COMPUTER SCIENCE

9608/13

Paper 1 Written Paper

May/June 2019

MARK SCHEME

Maximum Mark: 75

Published

This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began, which would have considered the acceptability of alternative answers.

Mark schemes should be read in conjunction with the question paper and the Principal Examiner Report for Teachers.

Cambridge International will not enter into discussions about these mark schemes.

Cambridge International is publishing the mark schemes for the May/June 2019 series for most Cambridge IGCSE™, Cambridge International A and AS Level and Cambridge Pre-U components, and some Cambridge O Level components.

This document consists of **10** printed pages.

Generic Marking Principles

These general marking principles must be applied by all examiners when marking candidate answers. They should be applied alongside the specific content of the mark scheme or generic level descriptors for a question. Each question paper and mark scheme will also comply with these marking principles.

GENERIC MARKING PRINCIPLE 1:

Marks must be awarded in line with:

- the specific content of the mark scheme or the generic level descriptors for the question
- the specific skills defined in the mark scheme or in the generic level descriptors for the question
- the standard of response required by a candidate as exemplified by the standardisation scripts.

GENERIC MARKING PRINCIPLE 2:

Marks awarded are always **whole marks** (not half marks, or other fractions).

GENERIC MARKING PRINCIPLE 3:

Marks must be awarded **positively**:

- marks are awarded for correct/valid answers, as defined in the mark scheme. However, credit is given for valid answers which go beyond the scope of the syllabus and mark scheme, referring to your Team Leader as appropriate
- marks are awarded when candidates clearly demonstrate what they know and can do
- marks are not deducted for errors
- marks are not deducted for omissions
- answers should only be judged on the quality of spelling, punctuation and grammar when these features are specifically assessed by the question as indicated by the mark scheme. The meaning, however, should be unambiguous.

GENERIC MARKING PRINCIPLE 4:

Rules must be applied consistently e.g. in situations where candidates have not followed instructions or in the application of generic level descriptors.

GENERIC MARKING PRINCIPLE 5:

Marks should be awarded using the full range of marks defined in the mark scheme for the question (however; the use of the full mark range may be limited according to the quality of the candidate responses seen).

GENERIC MARKING PRINCIPLE 6:

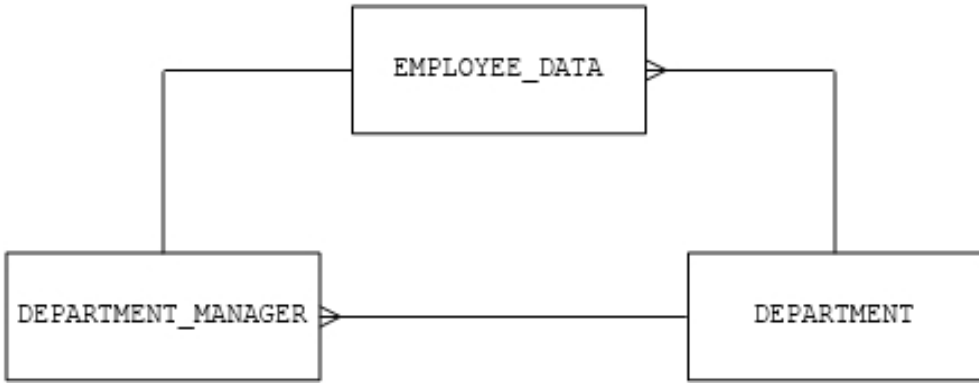
Marks awarded are based solely on the requirements as defined in the mark scheme. Marks should not be awarded with grade thresholds or grade descriptors in mind.

Question	Answer	Marks										
1(a)	<p>1 mark for each correct entry</p> <table><tr><th>Management task</th><th>Description</th></tr><tr><td>Memory management</td><td>Handles the allocation of memory to processes // Ensures two programs do not attempt to use the same memory locations // Keeps track of allocated and free memory locations</td></tr><tr><td>Security management</td><td>Provides user accounts and passwords</td></tr><tr><td>Interrupt processing</td><td>Handles the signals sent when the attention of the processor is required elsewhere</td></tr><tr><td>Provision of a software platform</td><td>Provides an environment within which programs can be run</td></tr></table>	Management task	Description	Memory management	Handles the allocation of memory to processes // Ensures two programs do not attempt to use the same memory locations // Keeps track of allocated and free memory locations	Security management	Provides user accounts and passwords	Interrupt processing	Handles the signals sent when the attention of the processor is required elsewhere	Provision of a software platform	Provides an environment within which programs can be run	4
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1(b)(i)	<p>1 mark per bullet point to max 2 for formatter, max 2 for defragmenter</p> <p>hard disk formatter</p> <ul style="list-style-type: none">• Makes existing data inaccessible• Partitions the disk into logical drives• Sets up the (specified) file system• Prepares the disk for initial use• May check for errors on the disk <p>hard disk defragmenter</p> <ul style="list-style-type: none">• Re-organises the disk contents• Moves split files so they are contiguous• Creates a larger area of (contiguous) free space	4										
1(b)(ii)	<p>1 mark per bullet point</p> <p>For example:</p> <ul style="list-style-type: none">• Backup software• File compression• Virus checker• Disk contents analysis / repair	3										

Question	Answer	Marks
2(a)	1 mark per bullet point <ul style="list-style-type: none"> Security: keep data safe from accidental/malicious damage/loss Privacy: keep data confidential // only seen by authorised personnel 	2
2(b)	1 mark for identifying method 2 marks for description to max 2 × 3 For example: <ul style="list-style-type: none"> User accounts User has a username and password Access to resources can be limited to specific accounts Cannot access <u>system</u> without valid username and password // prevents unauthorised access to the <u>system</u> Firewall All incoming and outgoing network traffic goes through firewall Blocks signals that do not meet requirements Keeps a log of signals Applications can have network access restricted Anti-malware Scans for malicious software Quarantines or deletes any malicious software found Scans can be scheduled at regular intervals Should be kept up to date Auditing Logging all actions/changes to the system In order to identify any unauthorised use Application Security (accept equivalent) Applying regular updates / patches Finding, fixing and preventing security vulnerabilities in any (installed) application 	6
2(c)(i)	1 mark per bullet to max 4 <ul style="list-style-type: none"> Each byte has a parity bit // horizontal parity An additional parity byte is sent with vertical (and horizontal) parity Each row and column must have an <u>even/odd number of 1s</u> Identify the incorrect row and column The intersection is the error 	4
2(c)(ii)	1 mark for correct answer <ul style="list-style-type: none"> Errors in an even number of bits (could cancel each other out) 	1

Question	Answer	Marks
2(d)	1 mark per bullet point to max 3 For example: He should ... <ul style="list-style-type: none"> • ...Keep the client's personal data private • ...Involve the client in the development // ...Communicate with the client • ...Provide the solution that the client asked for • ...Keep the project running on time // budget • ...Keep the client informed of any problems/delays 	3
2(e)	1 mark per bullet point to max 3 <ul style="list-style-type: none"> • He uses a compiler because it creates a separate executable file • The executable means the client cannot access the source code // edit the program • The executable means the client does not need the IDE / compiler • It will (probably) be faster to run the executable than to interpret the source code every time the program is run 	3

Question	Answer	Marks
3(a)(i)	1 mark per bullet point <ul style="list-style-type: none"> • Stores all the information about the database // data about the data // metadata about the data • For example, fields, data types, validation, keys 	2
3(a)(ii)	1 mark per bullet point to max 2 <ul style="list-style-type: none"> • Allows the user to enter criteria • Searches for data which meets the entered criteria • Organises the results to be displayed to the user 	2
3(b)	1 mark per bullet point to max 2 <ul style="list-style-type: none"> • Primary key uniquely identifies each tuple // Each tuple in the table is unique • Primary key can be used as a foreign key in another table • to form a link/relationship between the tables By example: <ul style="list-style-type: none"> • Identification of a primary key in a table • Describing <u>that</u> primary key in another table as a foreign key 	2

Question	Answer	Marks
3(c)	<p>1 mark for each correct link</p>  <pre> graph TD EMPLOYEE_DATA[EMPLOYEE_DATA] --- DM[DEPARTMENT_MANAGER] EMPLOYEE_DATA --- DEPT[DEPARTMENT] DM --- DEPT </pre>	3
3(d)	<p>1 mark per bullet point to max 3</p> <ul style="list-style-type: none"> • There are no repeating groups (1NF) • There are no partial dependencies (2NF) • There are no non-key dependencies // There are no transitive dependencies (3NF) 	3
3(e)(i)	<p>1 mark for correct answer</p> <pre>CREATE DATABASE EMPLOYEES;</pre>	1
3(e)(ii)	<p>1 mark per bullet</p> <ul style="list-style-type: none"> • Create table EMPLOYEE_DATA with open and close brackets • EmployeeID as VarChar restricted to max 7, Gender as a VarChar restricted to max 6, DepartmentNumber as a VarChar restricted to max 2 • FirstName and LastName as VarChar (any max lengths must be reasonable) • Date of birth as Date • Declaring EmployeeID as PK <pre>CREATE TABLE EMPLOYEE_DATA (EmployeeID VarChar(7), FirstName VarChar, LastName VarChar, DateOfBirth Date, Gender VarChar(6), DepartmentNumber VarChar(2), PRIMARY KEY (EmployeeID));</pre>	5

Question	Answer	Marks
3(e)(iii)	<p>1 mark per bullet</p> <ul style="list-style-type: none"> Select FirstName and LastName only From both tables Where DepartmentName = "Finance" AND Gender = "Female" Joining tables (either AND, or inner join) <pre>SELECT FirstName, LastName FROM EMPLOYEE_DATA, DEPARTMENT WHERE DepartmentName = "Finance" AND Gender = "Female" AND DEPARTMENT.DepartmentNumber = EMPLOYEE_DATA.DepartmentNumber;</pre>	5

Question	Answer	Marks																									
4(a)	<p>1 mark per bullet</p> <ul style="list-style-type: none">• LDM #300 The (denary) number 300 is loaded (into the register)• LDD 300 The <u>contents</u> of address 300 are loaded (into the register)	2																									
4(b)	<p>1 mark for JPN correct 1 mark for both of ADD and DEC correct 1 mark for LDR correct</p> <table><tr><th></th><th>Description</th><th>Jump instruction</th><th>Arithmetic operation</th><th>Data movement</th></tr><tr><td>LDR #3</td><td>Load the number 3 to the Index Register</td><td></td><td></td><td>✓</td></tr><tr><td>ADD #2</td><td>Add 2 to the Accumulator</td><td></td><td>✓</td><td></td></tr><tr><td>JPN 22</td><td>Move to the instruction at address 22</td><td>✓</td><td></td><td></td></tr><tr><td>DEC ACC</td><td>Subtract 1 from the Accumulator</td><td></td><td>✓</td><td></td></tr></table>		Description	Jump instruction	Arithmetic operation	Data movement	LDR #3	Load the number 3 to the Index Register			✓	ADD #2	Add 2 to the Accumulator		✓		JPN 22	Move to the instruction at address 22	✓			DEC ACC	Subtract 1 from the Accumulator		✓		3
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4(c)(i)	<p>1 mark for hardware interrupt 1 mark for software interrupt</p> <p>For example:</p> <p>Hardware interrupt</p> <ul style="list-style-type: none"> • Printer out of paper • No CD in drive <p>Software interrupt</p> <ul style="list-style-type: none"> • A running program needs input • Runtime error, e.g. division by zero 	2
4(c)(ii)	<p>1 mark per bullet to max 5</p> <ul style="list-style-type: none"> • At the start / end of each fetch-execute cycle the processor checks for interrupt(s) • Check if an interrupt flag is set // Check if bit set in interrupt register • Processor identifies source of interrupt • Processor checks priority of interrupt • If interrupt priority is high enough // Lower priority interrupts are disabled • Processor saves current contents of registers // saves current job on stack • Processor calls interrupt handler / Interrupt Service Routine (ISR) • Address of ISR is loaded into Program Counter (PC) • When servicing of interrupt complete, processor restores registers // job from stack is restored • Lower priority interrupts are re-enabled • Processor continues with next F–E cycle 	5

Question	Answer	Marks
5(a)	<p>1 mark for each correctly linked box on the right</p> <p>Maximum number of colours Minimum number of bits</p>	3
5(b)(i)	<p>1 mark for correct answer</p> <p>40 images or frames are <u>displayed/recorded</u> each second</p>	1
5(b)(ii)	<p>1 mark per bullet to max 4</p> <ul style="list-style-type: none"> • Progressive – each frame contains (all the lines for) the complete image • Interlaced – each frame contains half the (number of lines) of the complete image • Progressive – frames are not divided into fields • Interlaced – each frame divided into two fields // One field contains the data for the even numbered rows / lines and the other has the data for the odd numbered rows / lines. • Progressive – complete frames are displayed in sequence • Interlaced – data from two frames is displayed simultaneously • Progressive – the number of images stored is the same as the frame rate // the rate of picture display is the same as the frame rate. • Interlaced – the rate of picture display (the field rate) is twice the rate of image frame display (the frame rate). • Progressive means the entire frame is refreshed each time • Interlaced means only half the frame is refreshed • Progressive has high bandwidth requirements • Interlaced halves the transmission bandwidth requirements. 	4

Question	Answer	Marks
5(c)	<p>1 mark per bullet</p> <p>88.2 kHz</p> <ul style="list-style-type: none">The sound wave is sampled <u>88200</u> times per second <p>32 bits</p> <ul style="list-style-type: none">Each sample is stored as a 32-bit binary number	2